# (19) World Intellectual Property Organization

International Bureau





### (43) International Publication Date 30 June 2005 (30.06.2005)

### PCT

## (10) International Publication Number WO 2005/060196 A1

(51) International Patent Classification<sup>7</sup>:

H04L 27/38

(21) International Application Number:

PCT/JP2004/019462

(22) International Filing Date:

17 December 2004 (17.12.2004)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

2003906998 2004240146

17 December 2003 (17.12.2003) AU 15 December 2004 (15.12.2004) AU

- (71) Applicant (for all designated States except US): NEC Corporation [JP/JP]; 7-1, Shiba 5-chome, Minato-ku, Tokyo 1088001 (JP).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): CHUNG, Edwin [AU/AU]; c/o NEC Australia Pty. Ltd., 635 Ferntree Gully Road, Glen Waverley, Victoria 3150 (AU).
- Agent: MARUYAMA, Takao; Maruyama Patent Office, (74)SAM Build. 3floor, 38-23, Higashi-Ikebukuro 2-chome, Toshima-ku, Tokyo 1700013 (JP).

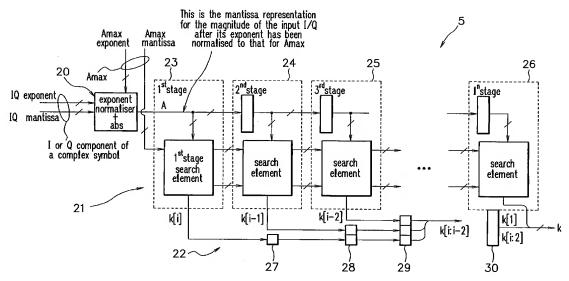
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

#### Published:

with international search report

[Continued on next page]

### (54) Title: DEMODULATION OF A MULTI-LEVEL QUADRATURE AMPLITUDE MODULATION SIGNAL



(57) Abstract: A device (5) for determining k representative of the magnitude A of an orthogonal component of a Quadrature Amplitude Modulation (QAM) symbol, including: multi-stage binary search circuitry (21) for conducting a multi-stage binary search for the value of A between predetermined maximum and minimum values Amax and Amin, each stage producing a single bit binary output; and integer value construction circuitry (22) for constructing the integer value k by juxtaposing the binary outputs from consecutive stages of the binary search, where W = (Amax - Amin)/ n, n equals 2i and i is an integer, Amax is a maximum detectable level of the magnitude A, Amin is a minimum detectable level of the magnitude A, and W is the incremental level between consecutive values of the integer value k.

### 

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.